**MMC QUESTION BANK**

1. Give the significance of page directory and page table in paging memory management of Pentium.
2. Explain IP and IE SFR of 8051
3. Write a note on bit addressable feature of 8051. Explain with the help of bit addressable locations of RAM.
4. List the address of SBUF SFR and explain it in detail..
5. Explain in detail port 3(P3) of 8051.
6. Describe the RAM structure of 8051.
7. Differentiate between conditional and unconditional jump with help of 8051 instructions.
8. With diagram brief all the bits of TMOD from 8051.
9. List and explain data transfer category of instruction set for 8051
10. Write an ALP program to display addition of 5, 8 bit numbers
11. What is multitasking and time sharing? Explain with example.
12. Show the calculation for generating timer values to generate 2 khz square wave for 8051.
13. Explain task linking with diagram.
14. Give 2 examples each of all addressing modes of 8051.
15. Explain the importance of CR2 and CR3 control registers of Pentium processor.
16. Linear address 95E08103H is generated by Pentium processor for paging enabled environment.
17. What will be the index value for PDE, PTE and page offset?
18. Explain use and working of TLB.
19. Draw TSS descriptor.
20. What is task register?. Explain its role.
21. Illustrate and Explain details of internal RAM of 8051.
22. List prominent features of microcontroller 8051.
23. Identify the addressing modes of following:

MOV A, @R1

MOV A, R1

MOV A, #67H

MOV A, 67H

1. Find out control word to be loaded in TMOD register to configure timer 1 in mode 2 for microcontroller 8051.

***Please note this is not an official Question Bank.***

***Source :*** *A collection of questions from remedial assignments from different Panels.*